

ABSTRACT

A video display apparatus has a synchronization signal generation circuit (1) for generating a synchronization signal of the second frame frequency (VO_SYNC); a conversion
5 frequency detector (2) for calculating a number of frames making up an unit block at each of the frame frequencies and a number of frames to be thinned based on the first frame frequency and the second frame frequency; a frame memory (4) for storing a first frame having the first frame frequency;
10 a difference detector (11) for comparing intensity data of each dot on the video display apparatus of a second frame which is currently input to the video display apparatus with intensity data of each dot of the first frame which is stored in the frame memory (4) and which is immediately before the
15 second frame, and detecting a difference between the two frames; a difference adder (12) for counting a number of dots for a case in which the difference of the intensity data detected by the difference detector (11) is greater than a prescribed value; a movement detection/judgment section (13)
20 for distinguishing whether or not a count value detected by the difference adder (12) is below a prescribed value and outputting a signal (13a) indicating that thinning of the second frame is possible, when the count value of the difference adder (12) is below the prescribed value; and a
25 frame thinning section (15) for thinning the second frame, in a case in which the signal (13a) indicating that frame thinning of the second frame is possible is output from the movement detection/judgment section (13) and also a signal (2a) indicating that the number of frames to be thinned is

output from the conversion frequency detector (2).

SECRET